



**DAYANANDA SAGAR
UNIVERSITY**



**SCHOOL OF
ENGINEERING**

**TIME TABLE FOR M.TECH (2024 BATCH) ODD SEMESTER
BACKLOG EXAMINATIONS - JANUARY 2026**

DATE	M.TECH - 1ST SEMESTER (TIMINGS: 09.30 AM TO 12.00 NOON)
	M.TECH (ECE)
08-01-2026 Thursday	Advanced Microcontroller and Programming (24ESE5101)
10-01-2026 Saturday	Digital CMOS VLSI Design (24ESE5102)
12-01-2026 Monday	Research Methodology and IPR (24ESE5105)
14-01-2026 Wednesday	Embedded System for Robotics (24ESE5107)
16-01-2026 Friday	Embedded Machine Learning (24ESE5109)

Ramdas 30/12/2025
CONTROLLER OF EXAMINATIONS

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DAYANANDA SAGAR UNIVERSITY
HAROHALLI**